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APPLICANTS

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 ** CONTINUING DATA *YES* *****

This application is a DIV of 10/183,834 06/25/2002 PAT 6,756,633
 which claims benefit of 60/343,634 12/27/2001
 and claims benefit of 60/355,363 02/06/2002

 ** FOREIGN APPLICATIONS *NO* *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 07/20/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged <i>Phan Bory</i> Examiner's Signature Initials	CA	31	3	2

ADDRESS

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TITLE

Method of operating a semiconductor memory array of floating gate memory cells with horizontally oriented edges

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